



Note:

This entire board should fit on a pcb about the size of a postage stamp. The battery should be placed on the back side of the pcb. A double sided board should be used. Small pieces of wire or copper strip placed over the edge of the board can be used to jumper the top to the bottom. J1 is a single row of .1" header pins soldered edgewise on the pcb. This makes it easier to plug into a breadboard for use, programming, and testing. Do not substitute the 93AAxx series serial eeprom unless you run the board from an external 5VDC power supply. Part numbers given are for SOIC surface mount.

*** It would be a good idea to design this board with the eeprom dip version of the 12C50X chip instead of the OTP surface mount, at least for program development and testing purposes. Use a standard dip socket (not the machine pin type), bend the leads out from the bottom and solder on as you would an SOIC, last spread the spacing on your dip layout to around .100 x .600 centers.

*** Last if anyone builds this thing before I do, please email me some of your experiences with it. I can be reached at: dave@kd0yu.com

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